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General purpose pulse shape analysis for fast scintillators implemented in digital readout electronics



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ABSTRACT

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1. Introduction

A variety of scintillators have the ability to discern between different types of radiation, such as neutron and gamma radiation fields. This pulse shape discrimination (PSD) capability is thought to arise in organic scintillators from variations in the delayed phosphorescence in high dE/dx events, i.e., those resulting from proton recoil [1]; in inorganic scintillators the quenching of fast fluorescence similarly modifies the pulse shape depending on particle type. As a result, neutrons produce a larger fraction of a slow scintillation light component than gammas in organic scintillators, which can be detected by suitable pulse shape analysis (PSA) methods. In addition, phoswich detectors, consisting of multiple layers of different scintillators, produce different pulse shapes depending on which layer absorbs the radiation, which can be used for particle identification [2-4]. Consequently, many materials and PSA algorithms have been proposed and studied in this context for many years [5–10]. Overall, the PSD tends to depend more on the choice of scintillator than the PSA algorithms, many of which produce similar figures of merit (FOM) [11,12], though processing complexity can vary widely. In addition to the choice of scintillator and PSA algorithms, the quality of the PSD also depends on the choice of the readout electronics. Traditionally, analog systems have been used and had been shown to perform better than early digitizers sampling at 40 MHz [13], though subsequent work demonstrated that digitizers that sample at 100 MHz or higher

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Pulse shape discrimination applied to certain fast scintillators is usually performed offline. In sufficiently high-event rate environments data transfer and storage become problematic, which suggests a different analysis approach. In response, we have implemented a general purpose pulse shape analysis algorithm in the XIA Pixie-500 and Pixie-500 Express digital spectrometers. In this implementation waveforms are processed in real time, reducing the pulse characteristics to a few pulse shape analysis parameters and eliminating time-consuming waveform transfer and storage. We discuss implementation of these features, their advantages, necessary trade-offs and performance. Measurements from bench top and experimental setups using fast scintillators and XIA processors are presented.

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outperform their analog counterparts [14]. Analog to digital converters with 12–14 bit precision are desired for best results [15,16]. While initially digital readout electronics has been used to simply capture detector waveforms for offline analysis [17,18], in recent years detector readout electronics have become more and more powerful, with increased digitization rate, precision, and on-board processing capabilities. The focus is thus shifting to performing PSD online in the readout electronics [19–21]. The main benefit is that it allows much higher throughput rates since waveforms do not have to be transferred from the electronics to hard drive or other storage. High throughput is of specific interest in assaying or monitoring special nuclear materials with fast scintillators [22], specifically those containing the prolific neutron emitter ²⁴⁰Pu, which emits 64 k neutrons/ s/kg. At (sustained) data rates of 10-100 MB/s with USB, PCI, or VME, transferring several hundred samples per waveform limits the throughput to 10,000-100,000 pulses/s, and throughputs may be lower in practice due to acquisition dead times. Newer I/O standards allow higher data rates, but newer digitizers also tend to operate at higher rates and thus generate more data. In contrast, if waveforms are processed online in a digital signal processor (DSP) or field programmable gate arrays (FPGA) and only a few result values have to be transferred per pulse, throughput can be orders of magnitudes higher; and if the results can be accumulated into spectra online, throughput is only limited by the processing speed of the electronics. With proper pipelining of the processing in the FPGA, in principle true real time processing of the signal is possible, with PSA values computed for every pulse "as it comes in". However, implementation is more cumbersome, especially on FPGAs, and the algorithms can only have limited complexity (e.g. divisions are difficult except for powers of two). It is also more difficult to modify the PSA for varying conditions



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or applications – a minor change in FPGA logic requires recompilation of the whole device, potentially producing timing errors and glitches in supposedly unchanged areas.

We previously implemented a very specific DSP pulse shape analysis for beta/gamma coincidence detection with a phoswich detector using the Pixie-4 [22,23], a digital data acquisition module made by XIA LLC. This code makes use of the user-programmable DSP function calls to customize the standard operation of the module. The functions have been shown to be also applicable to neutron/gamma PSD with CLYC and liquid scintillators [24]. In this work, as a first step, we have implemented a more general purpose version in the DSP of the Pixie-500 [25,26] and, as a second step, in the FPGA and DSP of the Pixie-500 Express. The new firmwares were then tested in several applications and compared to offline processing results. The approach used here is a digital version of the Charge Comparison Method [3,4], where two sums over characteristic regions of the pulse are accumulated and a suitable ratio expresses the difference in pulse shape. This is a well-established method used in a variety of applications, well suited for online processing due to its simplicity.

The Pixie-500 and Pixie-500 Express are 4-channel digital data acquisition modules based on the PXI and PXIe standards, respectively. A small analog section adjusts the incoming signal in gain and offset to match the input range of an analog to digital converter (ADC), and includes a Nyquist filter to limit the bandwidth to roughly half the digitization rate (resulting in a minimum rise time of 2-3 samples). The digital data stream from the ADC is processed in an FPGA for triggering, pile-up inspection, and filtering in real time, and optionally waveforms can be captured for offline analysis. A DSP performs event-by-event post-processing with more complex operations, such as corrections for the exponential decay of the pulses. Pulse heights are histogrammed in onboard memory. The Pixie-500 Express digitizes at 500 MHz with 14 bit precision (Pixie-500: 12 bit), and uses a 300 MHz 32 bit floating point DSP (Pixie-500: 75 MHz 16 bit fixed point). The Pixie-500 Express data connection to the host PC is via an $\times 4$ PCI Express link (Pixie-500: 32 bit PCI) that allows readout rates of several hundred MB/s, in parallel for each module in the chassis (Pixie-500: \sim 100 MB per chassis). To eliminate acquisition dead time, the Pixie-500 Express buffers results for several detected pulses in its Virtex-4 FPGA memory, so called zero dead time (ZDT) buffers, because the only remaining dead time is due to the minimum pulse separation enforced by the pile-up inspection to be able to measure 2 pulses without interference. When pileup inspection is disabled, closely following pulses and overlapping waveforms can be recorded.

2. Firmware implementation

2.1. DSP implementation with Pixie-500

As part of the event-by-event processing, the Pixie-500's DSP can read captured waveforms into local memory and execute user programmable functions. In this case, the functions compute baseline average sum B, amplitude A, two sums Q0 and Q1 over characteristic areas of the pulse, and rise time RT, as shown in Fig. 1. The first 8-16 samples of the waveform are summed and normalized to obtain B. The maximum sample in the waveform is located by a sample by sample search in a waveform sub region defined by the user prior to data acquisition (to reduce the processing time), then subtracted by *B* to obtain *A*. From the maximum towards earlier points, the waveform is then searched for the samples before and after the 10% and 90% constant fraction level of A. Four input parameters (L0, L1, S0, S1), specified prior to the data acquisition, define the length and position of the sums Q0 and Q1 relative to the 10% threshold, as shown in Fig. 1. The sums Q0 and Q1 are baseline subtracted by B scaled according to their length L0 and L1, respectively. The rise time is the time difference between the 10% and 90% level of the pulse, computed with linear interpolation from the ADC samples just below and just above each threshold to a precision of 1/16 of a sample. Intermediate steps are performed using 32 bit words to retain precision before downshifting to extract the final result. A sixth return value, or PSA ratio, can be selected by the user to best match a specific application. In what follows we generally use the ratio R1 = 01/00, but other ratios have been implemented as well. All told, these data, plus the timestamp and overall pulse height *E* computed with a trapezoidal filter, are written into an event header, followed by optional storage of the full waveform. From the number of instructions in the DSP code, we compute the throughput limit to be \sim 45,000 pulses/s for 150 samples per waveform, and roughly inverse proportional to the number of samples in the waveform. In comparison, reading full waveform data through the PCI interface limits the throughput to 1 000-5000 pulses/s.



Fig. 1. PSA input and output parameters. The DSP or FPGA compute baseline *B*, amplitude *A*, rise time RT and sums Q0 and Q1 using user defined delays from trigger point S0, S1 and lengths *L*0 *L*1.

2.2. FPGA implementation with Pixie-500 Express

Modern FPGAs can accept very high rate input signals, but internal processing is limited by logic and routing delays to rates of roughly 200 MHz (For example, an N-bit adder requires for each bit the carry from the previous bit sum, so that the end result is available only after N \sim 0.1 ns logic operations plus routing delays). In the Pixie-500 Express, the 14 bit, 500 MHz data stream from the ADC is therefore processed in sets of 4 samples at 125 MHz, for example, to generate a coarse trigger T at the rising edge of a pulse with a precision of 8 ns (see Fig. 2). For the PSA functions in the FPGA, a 4-sample sum of incoming data is thus the basic building block for the PSA sums. The 4-sample sum is formed with 2 adders summing 2 samples, each in one processing cycle, and a third adder summing the 2-sample sums in the following processing cycle. The resulting 4-sample sum is thus updated every processing cycle, but is 2 cycles delayed to the incoming data. For simplicity, the base length is fixed to 8 samples, and the base sum is computed continuously as the sum of two 4-sample sums, latched with appropriate delay into a ZDT output buffer by the trigger. In a first implementation (referred to below as FPGA_1), the PSA sums Qi were limited a) to start with a fixed delay Si relative to the coarse trigger, b) to have delays Si of multiples of 4 samples, and c) to have lengths Li of multiples of 4 samples. At every trigger, the process of summing is started by clearing the sum register and loading a counter with (Li+Si)/4. When the counter reaches Li/4 the 4-sample sums are added to the PSA sum until the counter reaches 0. The PSA sum is latched into a ZDT output buffer by a delayed signal of the coarse trigger. Two such summing circuits are implemented for the two sums, allowing their length and delay to be set independently. In a subsequent revision (FPGA_2), limitations a) and b) were removed. This required complex logic for processing the 4 samples within each set, often 4 circuits in parallel, and was not applied to limitation c) since that was found to be not unduly restrictive in practice. For a), a separate leading edge trigger was implemented for each of the 4 samples in a set. The 4-sample sum was assembled from 4 samples out of the current and previous set depending on which sample triggered first, i.e. shifting the 4-sample sum forward 0-3 samples if sample 0-3 triggered first, respectively. For b), a second such shifter was added, based on the lowest 2 bits of the user specified delay. In addition, peak capture logic was added to record the maximum value of the signal during (L1+S1)/4, the longer and later of the two sums. In this firmware implementation mode the DSP is relegated to reading out the base sum *B*, the two PSA sums, and the maximum from the FPGA ZDT buffers as part of the overall event processing, then subtracting *B* from the PSA sums and the maximum to compute A, QO, and Q1. In FPGA_2, the DSP also computes $1000 \times R1$, the factor 1000 introduced to provide more significant digits than available with integers. Other ratios can relatively easily be added by modifying only the DSP code. Preliminary tests indicate the throughput limit to be above 500,000 pulses/s, mainly depending on the time spent by the DSP to read raw PSA values from the FPGA and write the end results into the output data stream (but not on the

waveform length). Currently no CFD trigger or rise time computation is implemented in the FPGA.

3. Results and discussion

3.1. Pulser tests

We first tested the online PSA implementations under idealized conditions using a pulser signal. A mixed neutron/gamma field was simulated by programming the FPGA of an XIA PXI-PDM (pulser) to produce a digital data stream of randomly triggered pulses with differing rise and decay times but identical amplitudes, which are output as analog signals via an on-board digital to analog converter.

List mode data was acquired with the Pixie-500 Express (online FPGA_1, online FPGA_2 and offline) and the Pixie-500 (online DSP and offline). For online methods, R1 was computed in the respective DSP as described above, whereas for the offline methods, R1 was computed by equivalent algorithms in IGOR Pro. For each method, R1 values were histogrammed for $\sim 20,000-50,000$ pulser events as shown in Fig. 3. The Pixie-500 DSP (dashed gray) and offline (dashed black) give very close results, as expected. The Pixie-500 Express first FPGA implementation (FPGA1, solid light gray) performs poorly, producing broad, asymmetric peaks. The second FPGA implementation (FPGA_2, solid dark gray) performs the best of all approaches, producing very narrow peaks and nearly identical to the offline computation (solid black). Clearly an FPGA-based approach to PSA, if implemented carefully, is competitive with other approaches, while obviating the need for waveform capture and storage. In the following, we explore whether this performance is maintained under more realistic conditions. We continue to write waveforms to disk only so that we may directly compare the same data set.

3.2. EJ-309 neutron/gamma PSD

The Pixie-500 Express firmware (FPGA_1) was tested with the signal from several channels of a 12-detector array of liquid scintillators (EJ-309, Eljen Technologies) at AWE Plc. A ²⁵²Cf source was placed at the center of the array. The Pixie-500 Express captured waveforms for offline analysis and at the same time computed the PSA sums in the FPGA. The PSA parameters were set to L0/L1/S0/S1 = 20/40/0/36after brief trail-and-error for optimizing the separation of the gamma and neutron branches. Fig. 4 shows the online results plotted as R1 vs *E*, with the ratio computed from the sums during the plotting process, and the histogram of R1 (projection to left axis). Neutrons and gammas form two branches in the scatter plot, as commonly observed in such experiments. The captured waveforms were then processed offline for comparison. The offline processing allowed variation of PSA sum lengths and positions, as well as the different trigger definitions to detect the rising edge of the pulse. The scatter plot for L0/L1/S0/S1=20/40/0/36 is shown in Fig. 5, using a "fine" trigger regarding



Fig. 2. Block diagram of FPGA implementation described in the text. Gray boxes are only present in implementation FPGA_2.



Fig. 3. A comparison of PSA ratios for offline versus online data processing using a fixed amplitude pulser for the input signal. The lower peaks correspond to gamma-like signals (solid line in inset), the upper ones to neutron-like signals (dashed). The text describes the various acquisition modes.



Fig. 4. PSA scatter plot for neutron/gamma separation from an EJ-309 scintillator and a ²⁵²Cf source. PSA sums and energy were computed online with the Pixie-500 Express in the FPGA with the 4-sample coarse triggering. The ratio was computed offline.



Fig. 5. PSA scatter plot for neutron/gamma separation from an EJ-309 scintillator and a ²⁵²Cf source, computed offline from waveforms acquired with Pixie-500 Express.

every sample, not every 4th. The offline computation produces visibly narrower distributions for the gamma and neutron branches.

To quantify the differences, we use the standard definition of the FoM, defined as (Pn - Pg)/(Sn + Sg), where Pn is the peak of the neutron distribution, and Sn the FWHM of the neutron peak, and similarly for the gamma peak (Fig. 6). All events down to the



Fig. 6. FOM as a function of trigger position relative to the leading edge of the pulse. Coarse triggering within a set of 4 samples is shown with error bars of ± 3 . Fine online triggering is equal to fine offline triggering (in a second data set).

minimum recorded pulse height are included. For (coarse trigger) online processing, the FoM is 0.38 ± 0.04 compared to 0.65 ± 0.04 for offline processing with the fine trigger. (A major source of FoM uncertainty is the fitting of the rather poorly resolved double peak in the histogram.) When shifting the trigger position in online processing by ± 3 samples (gray triangles in Fig. 6), it can be seen that a late trigger significantly reduces FoM, emphasizing the importance of the first few samples on the rising edge of the pulse. As the coarse online trigger effectively jitters by ± 3 samples (horizontal error bars in Fig. 6) due to the random position of the rising edge within a set of 4 samples, its overall FoM is essentially an average of that range. Purposely coarsening the trigger in offline processing confirms the reduction of the FoM. It is also observed that use of a constant fraction trigger (gray diamonds in Fig. 6) results in lower FoM than the fine or coarse trigger.

The FoM can be improved by applying a low energy cutoff to the data. A cutoff of ~200 keV (removing ~40% of the events) is required to make the coarse offline FoM equal to the offline fine FOM (0.67). FoM values for neutron/gamma discrimination reported in the literature fall in the range of ~1.0 to ~2.0 [16,27] for EJ-309 and ~1.0 to ~2.7 for EJ-301 [28], but are often given for a narrow energy range, and are found to vary by source, geometry, as well as digitization precision and rate [16,28] (the latter implying a certain signal bandwidth). PSD parameter definition may also vary from *R*1 used here,

which makes meaningful comparison difficult. When the PSA parameters were optimized offline, the largest FoM for our data was 1.14 ± 0.04 for L0/L1/S0/S1 = 8/40/0/16. Very broad optima were observed, in particular for variations in length. Based on this result, we found it unnecessary to remove the limitation of PSA length being a multiple of 4. Finer online triggering is however highly desirable, hence the second implementation of the FPGA firmware. A second data set acquired with implementation FPGA_2 achieved equal FoM for fine online and fine offline triggering (open circles in Fig. 6)

3.3. Stilbene gamma/alpha PSD

A material more recently developed for pulse shape analysis is crystal Stilbene [29]. While the main purpose of the materials development is neutron/gamma discrimination, in our work we explored it as a gamma/alpha discriminator. Being a solid rather than a liquid, it requires no container, and so an alpha source can easily be brought in close enough contact with the scintillator. In this test, a small flat crystal of Stilbene (Inrad Optics, approx. $2 \text{ mm} \times 2 \text{ cm} \times 1 \text{ cm}$) was coupled to a 1" PMT (Hamamatsu) with optical grease. The other side was covered with 4-8 layers of Teflon tape. A small gap was left in the taped area, over which a ²⁴¹Am source was placed, emitting alphas and photons. The whole PMT/scintillator/source arrangement was then placed in a light tight box. The PMT is biased to \sim 950 V with an iseg EHQ105M HV module and read out by a Pixie-500 or Pixie-500 Express which performed the PSA (DSP or FPGA_2) and captured waveforms for offline comparison. A ¹³⁷Cs source was put on the outside of the box to increase the photon rate.

PSA scatter plots for online and offline computations are shown in Fig. 7 for optimized parameter settings L0/L1/S0/S1 = 12/64/0/32. Alphas excite more of the slow scintillation process and therefore have relatively larger Q1, populating the upper group of events in the scatter plots. Online and offline scatter plots for the Pixie-500 Express (using the same data) look almost identical. In the online data, "bad" events (often with negative values due to noise spikes in the baseline) appear at 64 K, a total of 812 out of ~470,000 in this data set. In the offline data, 827 events are "bad" and their negative ratios do not appear on the log scale graph. Coarseness of integer numbers becomes visible for R1 < 10 in online computation, but this affects only a small minority of counts.

The data from Fig. 7Figure is histogrammed in Fig. 8. For the Pixie-500 Express measurements, the FoM is 1.393 for Pixie-500



Fig. 7. Scatter plots of *R*1 vs *E* for Stilbene with Pixie-500 and Pixie-500e. Pixie-500e FPGA_2 and offline results are from the same measurement.



Fig. 8. Histograms of PSA ratio R1 for data shown in Fig. 7. Pixie-500 Express online and offline distributions are essentially identical, Pixie-500 online distribution is from a measurement with fewer recorded events.

Express online computation, 1.392 for Pixie-500 Express offline computation, and 1.089 for Pixie-500 DSP online computation. With clear double peaks, the uncertainties are \sim 0.03 or less. The Pixie-500 Express histograms are essentially identical (except the online implementation of the division returns negative numbers near 64K and so its histogram ends at zero). It is to be expected that the FOM for the Pixie-500 DSP online computation is close to that for the Pixie-500 Express offline computation as both methods are operating on equivalent waveforms. Yet, it is somewhat poorer than those for the Pixie-500 Express since implementation in the DSP, like the FPGA requires certain tradeoffs (here, numerical precision and limited filter lengths). In particular, the triggering process is different, the DSP employing constant fraction thresholds for triggering and rise time computation, which may be more sensitive to the particular threshold level used.

It is also instructive to compare the differences in Pixie-500 Express online and offline results for each individual parameter. To do so, we compute the difference $(X_{\text{online}} - X_{\text{offline}})/X_{\text{offline}}$ for each pulse, (X stands for Q0, Q1, A, B, or R1) and accumulate it in a histogram for all pulses in the data set. Q0 and Q1 here are not baseline subtracted to avoid addition of error from the baseline. The histograms are shown in Fig. 9. Base and Q1 differ very little, as the signal changes very slowly in the baseline and the falling portion of the pulse. The amplitude differs slightly more, however since the maximum is found accurately without depending on trigger position, the baseline subtraction adds the majority of differences. Q0 differs more, due to the fast rising edge, and the ratio has the largest differences due to the accumulation of differences from Q0, Q1 and base. Overall, > 98% of pulses have less than 1% difference in Q0, Q1, amplitude or base, and \sim 95% of pulses have less than 10% difference in R1, see Table 1. We note that even with 10% differences between online and offline values for R1 in individual pulses, the summary scatter plot, histogram and FoM show no significant differences.

3.4. CsI/Stilbene alpha/beta/gamma PSD

The Pixie-500 Express firmware (FPGA_1) was tested also with a phoswich detector for detection of radioactive gases. Originally designed to study Al_2O_3 coating of BC-404 scintillators to reduce diffusion of radioxenon [30], it was now assembled as a 1 mm thick \times 50.8 mm diameter crystal Stilbene disc (Inrad Optics) optically coupled to a 50.8 mm tall \times 50.8 mm diameter Csl(Tl) crystal, read out by a single PMT (ET Enterprises KB9266). The Stilbene is one end of an otherwise Aluminum cell holding radioactive gas samples. Exposed to a ²²²Rn source (with daughters), we observe 4 types of signals from this detector shown in Fig. 10: 1) gammas interacting in the Csl, 2)



Fig. 9. Histogram of normalized differences between online and offline computation of PSA results. Differences are small for amplitude, base, and Q1, larger for Q1 and PSA ratio R1.

Table 1

Quantification of difference between online and offline computation.

Parameter	Fraction of counts within range around zero		
	1%	2%	10%
Q0 ^a Q1 ^a Amplitude Base Q1/Q0	0.976 0.996 0.985 0.999 0.274	0.991 0.998 0.995 1 0.495	0.998 1 0.998 1 0.947

^a Not baseline subtracted.



Fig. 10. Waveforms from Csl/Stilbene phoswich detector. 4 different types can be identified, as described in the main text. Waveforms are smoothed after 1.1 μ for clarity.



betas (or gammas) interacting in the Stilbene, 3) alphas interacting in the Stilbene, and 4) gamma/beta coincidences in CsI and Stilbene (or gammas scattering between CsI and Stilbene). Gammas interacting in the CsI generate a slow rising pulse with \sim 1 us decay. Betas and gammas interacting in Stilbene generate the same pulse shape, a fast rise followed by a fast decay. Alphas generate a pulse shape with a fast rise and a slightly slower decay, due to a larger contribution of the slow decay component. CsI/Stilbene coincidences generate a mixture of the respective pure pulses, the relative contribution proportional to the deposited energy in each scintillator. The PSA parameters were tuned to L0/L1/S0/S1 = 20/40/0/36 for online computation of Q0, Q1 and B (FPGA_1), and E was computed as usual by the trapezoidal

filter. The ratios Q0/E and Q1/E were computed offline and are plotted in Fig. 11 (left). The 4 event types fall into well separated groups, with Csl/Stilbene coincidences connecting the groups of pure Csl events and pure Stilbene beta (or gamma) events. In contrast, exposing the detector to an external ¹³⁷Cs source (no alphas) results in a similar plot, but without the alpha events, Fig. 11 (right). The detector and PSA thus allow a 2-layer phoswich to detect alphas, betas and gammas, while in the past 3-layers have been used for such purposes [2].

In this case, a FoM defined as in the above applications would be less meaningful, since there are more than 2 event types. With appropriate regions of interest applied to the distribution in Fig. 10, the pulse heights could be histogrammed separately for the different even types, to obtain pure gamma, beta, and alpha spectra from the same measurement. We note that while digitization at 500 MHz is helpful in distinguishing Stilbene alphas and betas, the larger noise associated with the high bandwidth and the sensitivity to the arrival of randomly emitted photons complicates the analysis of CsI pulses, e.g. by making it harder to trigger reliably on small pulses.

4. Conclusions

In summary, we have implemented a general purpose PSA analysis in firmware and have compared the results with those from offline implementations using a pulser and a combination of sources and fast scintillators capable of discriminating between radiation fields. Broadly speaking, even the first FPGA implementation is useful in some applications, and the DSP and the second firmware implementations (with CFD or fine triggering), provided nearly equivalent results to their offline counterparts – the overall scatter plots, histograms, and FoMs are practically identical. This demonstrates that a general purpose PSA FPGA implementation is feasible and readily adaptable to other fast scintillators, while allowing 1–2 orders of magnitude higher throughputs. Implementation in the DSP is comparatively easier,



since sequential programming is used, but processing is slower, especially where sample by sample operations on buffered waveforms are performed. Implementation in the FPGA is more complex and requires great care to debug errors and adapt to device limitations, but processing throughput is much faster. In practice, a combination of FPGA, DSP and offline processing seems to be the most rewarding – the FPGA performing the simpler, but time consuming operations such as summing and finding maxima, while computation of ratios and acceptance decisions can be performed by a DSP or offline. This also allows more flexibility for varying PSD criteria in different applications. One simple extension of this work would be the introduction of a threshold for accepting events (e.g., those above a certain PSA ratio threshold) and building neutron and gamma histograms by the DSP in on-board memory.

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