



Parallel plate avalanche counter (PPAC) development and performance test for Rare Isotope Science Project (RISP)

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RISP, IBS

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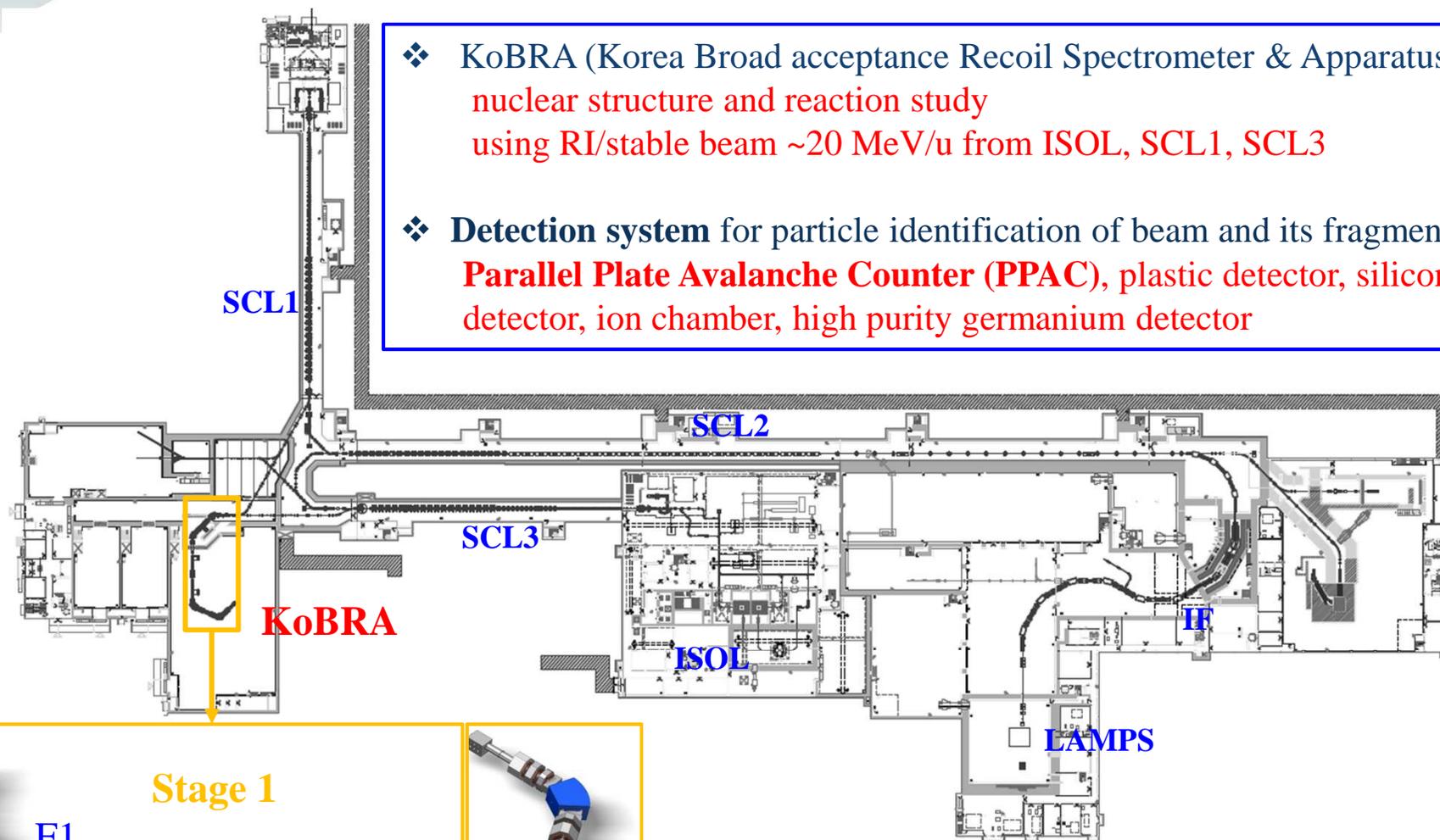
ICABU2017

*21th International Conference on Accelerators and Beam Utilization
November 15 ~ 17, 2017 • HICO Gyeongju • Korea*

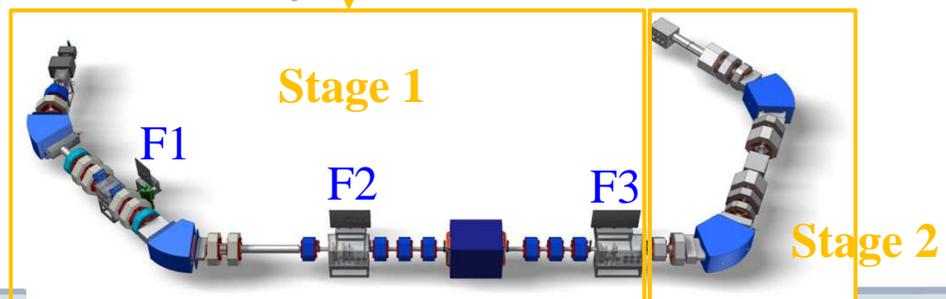


- ❖ RAON facility & KoBRA layout
- ❖ Detection system & PPAC
- ❖ PPAC structure
 - Delayboard, electrode and its assembly
- ❖ Performance test & analysis status
 - α of ^{241}Am , 3 MeV/u ^{12}C and ^{16}O beam
- ❖ Summary and outlook

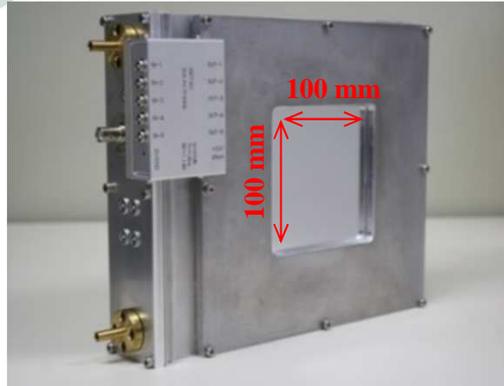
RAON facility Layout



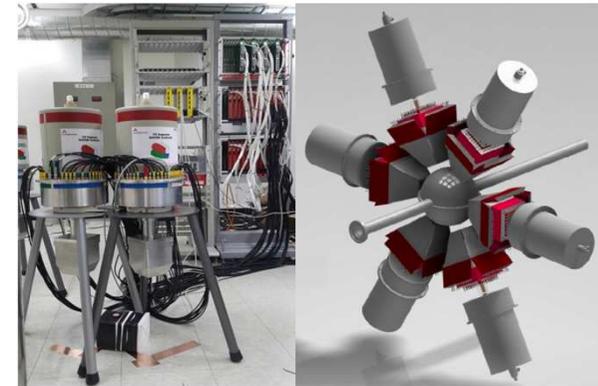
- ❖ KoBRA (Korea Broad acceptance Recoil Spectrometer & Apparatus); nuclear structure and reaction study using RI/stable beam ~ 20 MeV/u from ISOL, SCL1, SCL3
- ❖ Detection system for particle identification of beam and its fragments; Parallel Plate Avalanche Counter (PPAC), plastic detector, silicon detector, ion chamber, high purity germanium detector



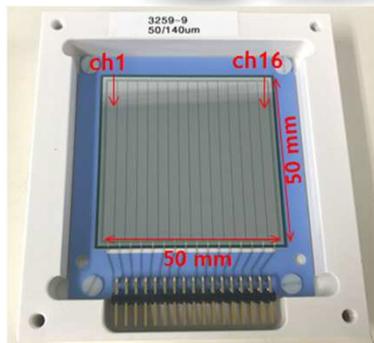
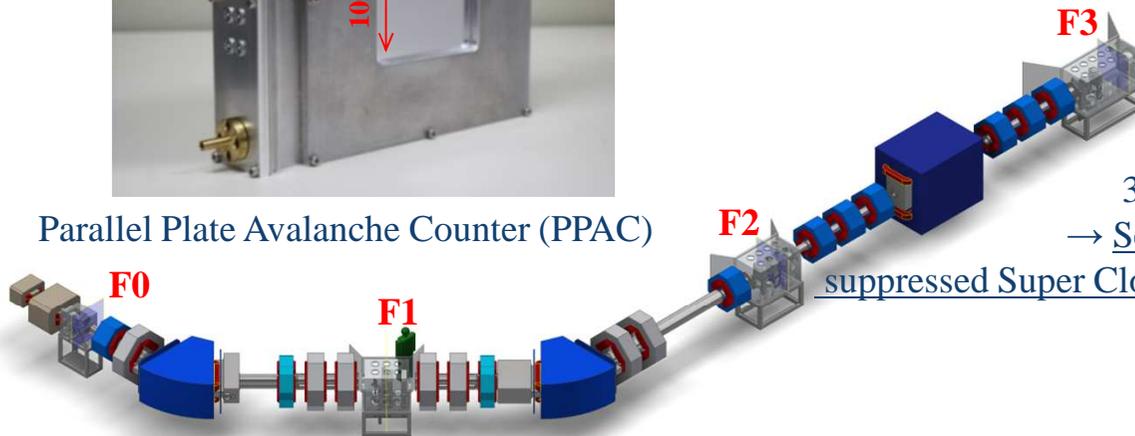
KOBRA detection system



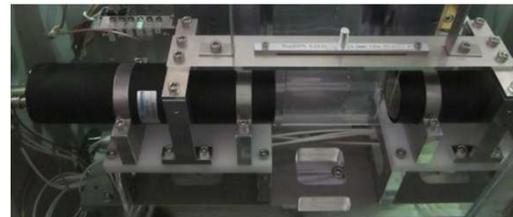
Parallel Plate Avalanche Counter (PPAC)



32-segmented high purity germanium detector
 → See 'PA-53: Development Status of Compton-suppressed Super Clover Detector Array in RISP' @ Poster session



16 ch. silicon detector



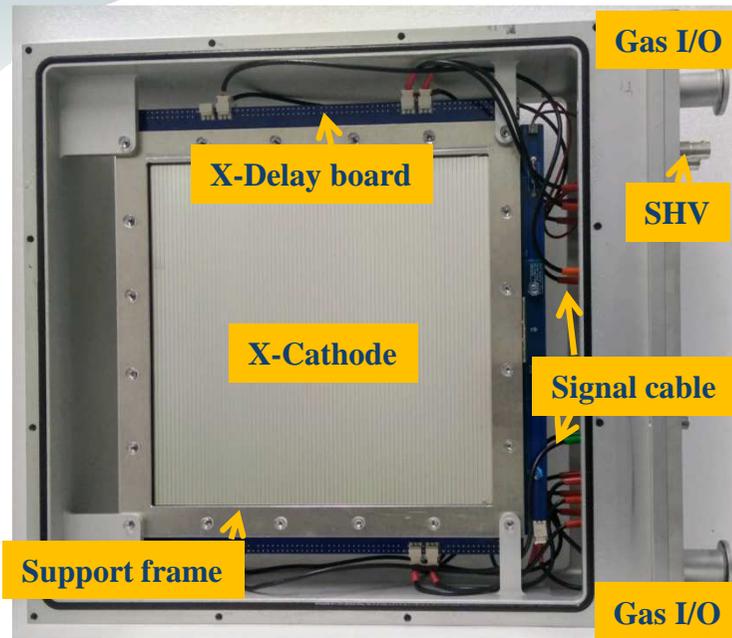
Plastic detector



Ionization chamber

- ❖ Detectors measure beam position, time, energy for particle identification of $B\rho$ -TOF- ΔE method;
 - PPAC: position for $B\rho$** , Plastic detector: time for TOF, Silicon detector: ΔE
 - ← $10 \times 10 \text{ cm}^2$, $20 \times 20 \text{ cm}^2$, $40 \times 20 \text{ cm}^2$ active area PPACs are developed

PPAC structure



Inside of PPAC
(covers opened)

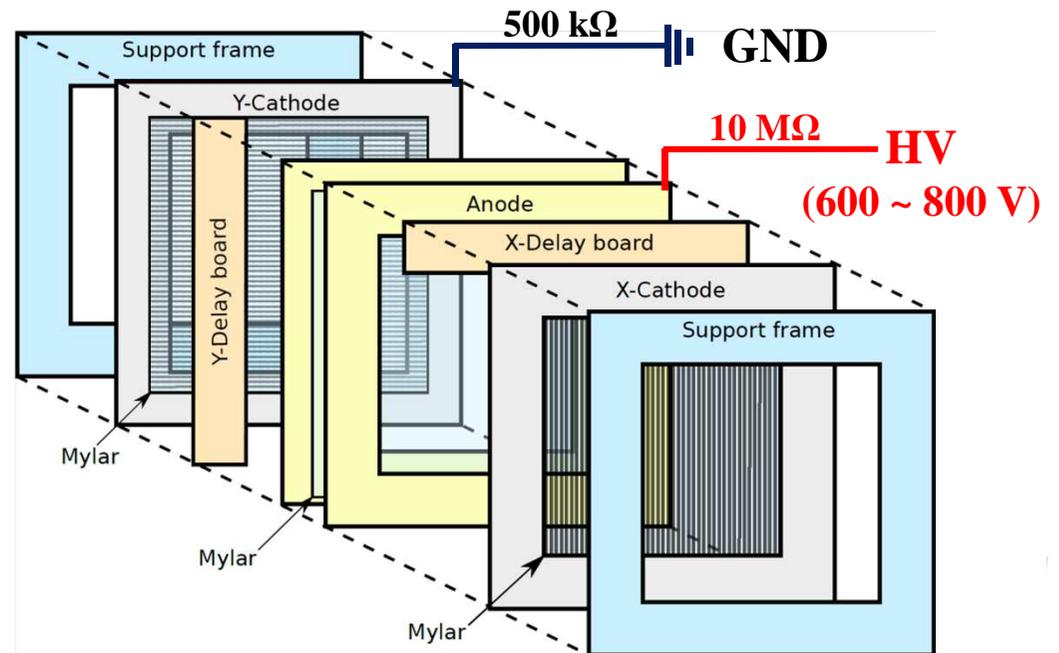
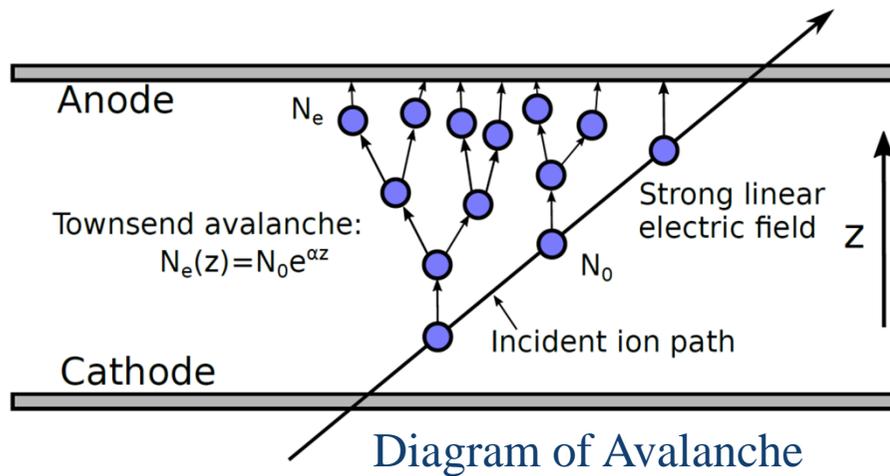


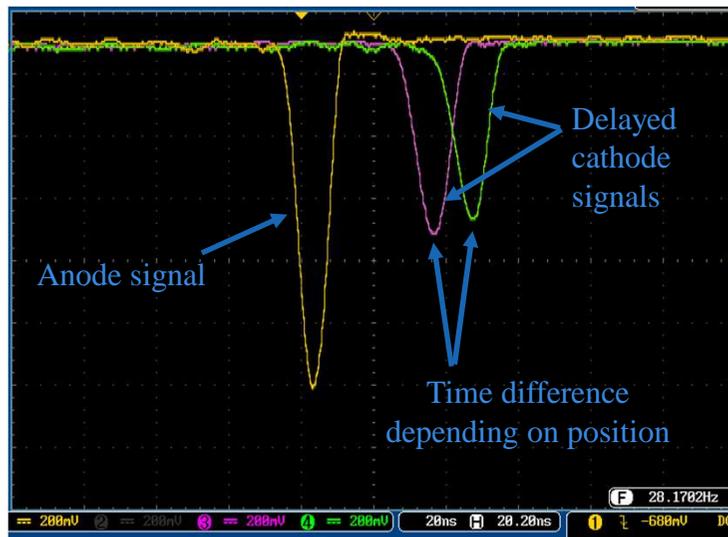
Diagram of a board layers

- ❖ PPAC is a gas detector using electron avalanche between electrodes
- ❖ It has fast response of ~few ns using fast electron
→ shows good performance even for the high beam intensity
- ❖ It operates for low energy experiment
→ needs to minimize interference in beam path;
thin Mylar of 2~3.5 μm for the electrodes
low gas pressure mode less than 10 Torr

Operating conditions



- ❖ Gas available;
Need pure quenching gas of C_4H_{10} ($W = 23.7$ eV), C_3F_8 ($W = 34.4$ eV)
due to lack of quenching properties at low gas pressure
- ❖ HV; ≥ 100 V/mm, adjustable by beam energy & mass
- ❖ Read out avalanche signals from anode and cathode;
Anode for time & triggering, cathode for position measurement

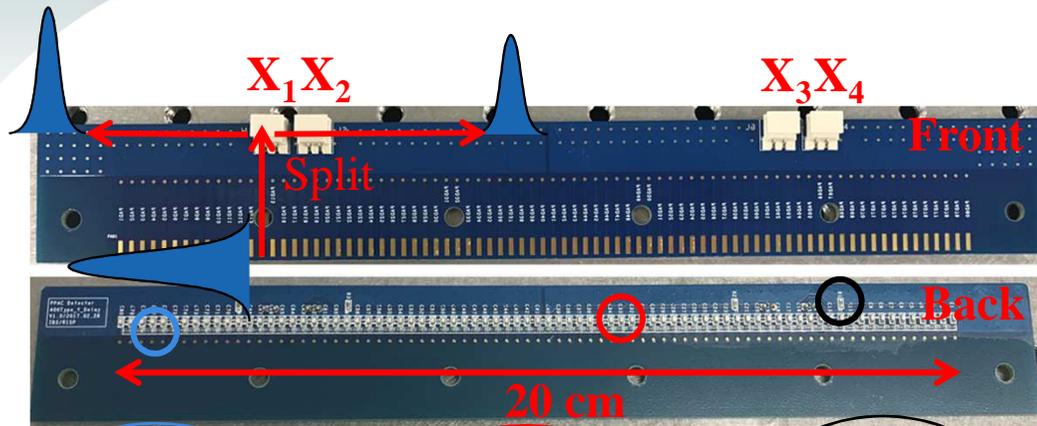


Signal example after amplifier

Readout method

- ❖ Charge division;
Resistive arrays gives charge difference, but few μs shaping time
→ charge pile-up effect at few kHz beam rate
- ❖ Delay line;
LC delay makes time difference from either end using fast ns signal
→ fast measurement for few MHz beam rate

Delay line readout



For 20 cm area PPAC;

- ❖ 2.5 mm/tap makes 80 LC taps, 2 ns tap delay,
→ 160 ns delay in total
- ❖ Impedence, $Z_0 = \sqrt{\frac{L}{C}} = 48 \Omega$
- ❖ 2 separate sections to reduce signal attenuation
- ❖ Precise chip inductor, capacitor of $\sim 2\%$ tolerance, negligible compared to detector resolution



0603 chip inductor
91 nH (2% tolerance)

L

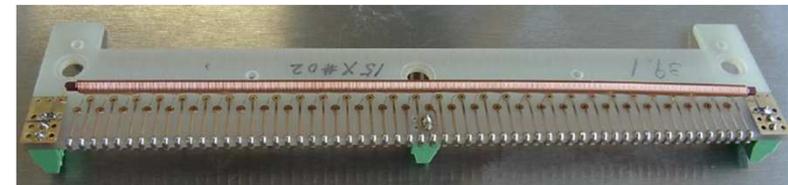


0603 chip capacitor
39 pF (1% tolerance)

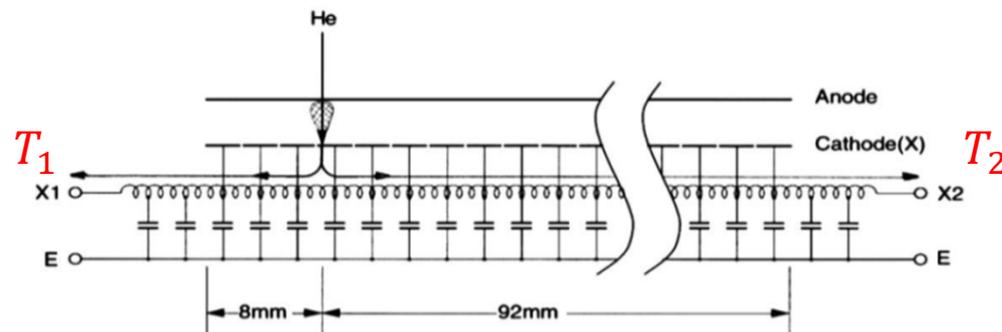
C



0603 chip resistor,
500 k Ω



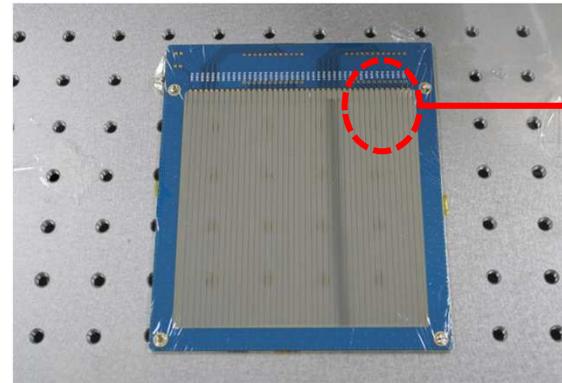
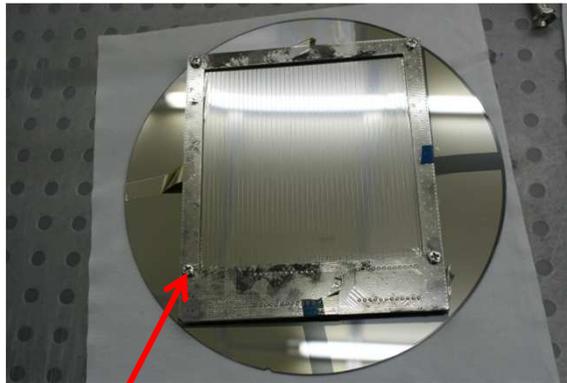
cf) delay line of RIKEN PPAC by coiling
- hard to control and produce



LC chain signal flow

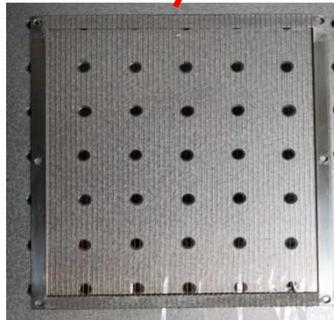
$$\text{Position extracted by } x = k(T_2 - T_1) + x_0$$

Electrode evaporation - cathode



Zoom-in

Ag evaporation on Mylar for cathode strip



Mechanical masking frame

- ❖ Cathode contacts to delay line, needs strip pattern to separate hit position
- ❖ Evaporate Ag/Al on the masking frame
- ❖ Take off the masking after evaporation
- ❖ This method is available for $10 \times 10 \text{ cm}^2$ cathode

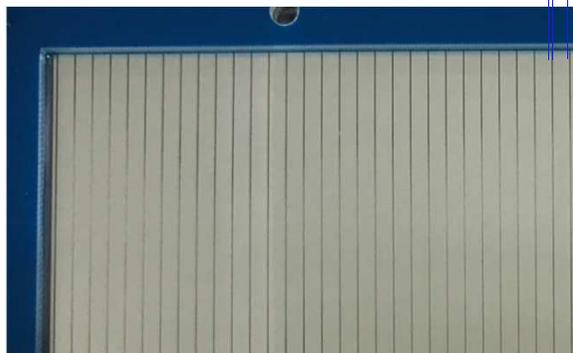
- ❖ It gets tricky technically for large area electrode
- ❖ Mechanical masking frame gets distorted larger than $20 \times 20 \text{ cm}^2$

Large area electrode

0.15 mm 2.35 mm



Cathode strip by
mechanical masking
-blurred, electrically connected



Cathode strip by
Photolithography,
newly tired method

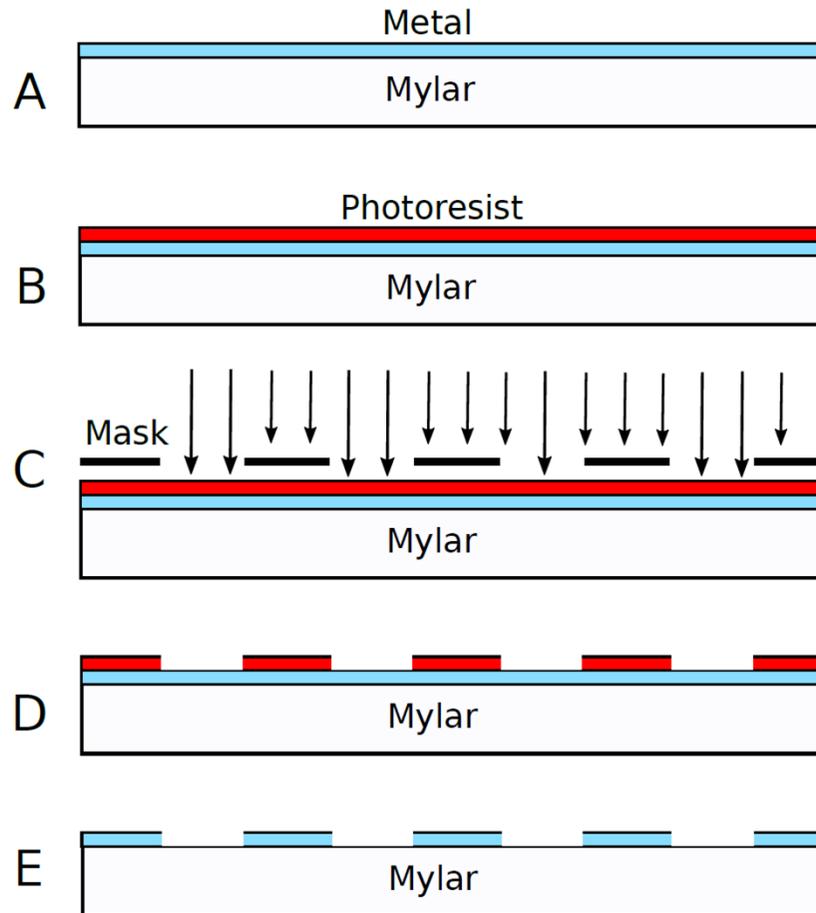
Good shape obtained
for $20 \times 20 \text{ cm}^2$ and $40 \times 20 \text{ cm}^2$



Large area cathode electrode

Largest electrode tried for PPAC
Technically possible up to
 $50 \times 50 \text{ cm}^2$

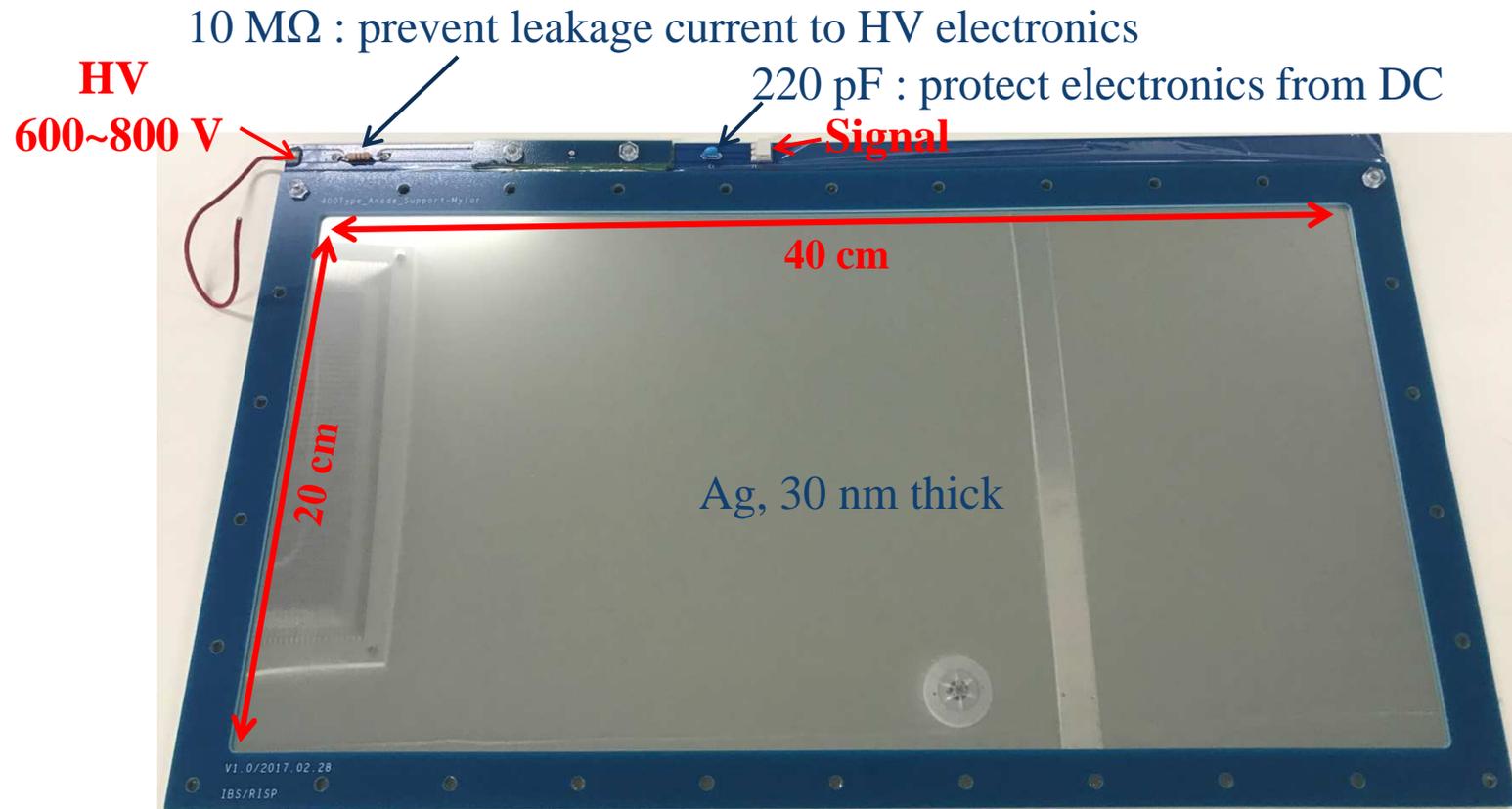
Large area electrode - Photolithography



- ❖ Evaporate Ag/Al 30 nm on top of 3.5 μm thick Mylar for $40 \times 20 \text{ cm}^2$, 2.5 μm thick Mylar for $20 \times 20 \text{ cm}^2$ (~3 nm thickness uncertainty)
- ❖ Coat with photoresist & bake for dry
- ❖ Mount film mask, expose UV light
- Outside of mask, photoresist becomes soluble
- ❖ Develop and remove photoresist exposed to UV light
- ❖ Etch as pattern and remove the remaining photoresist

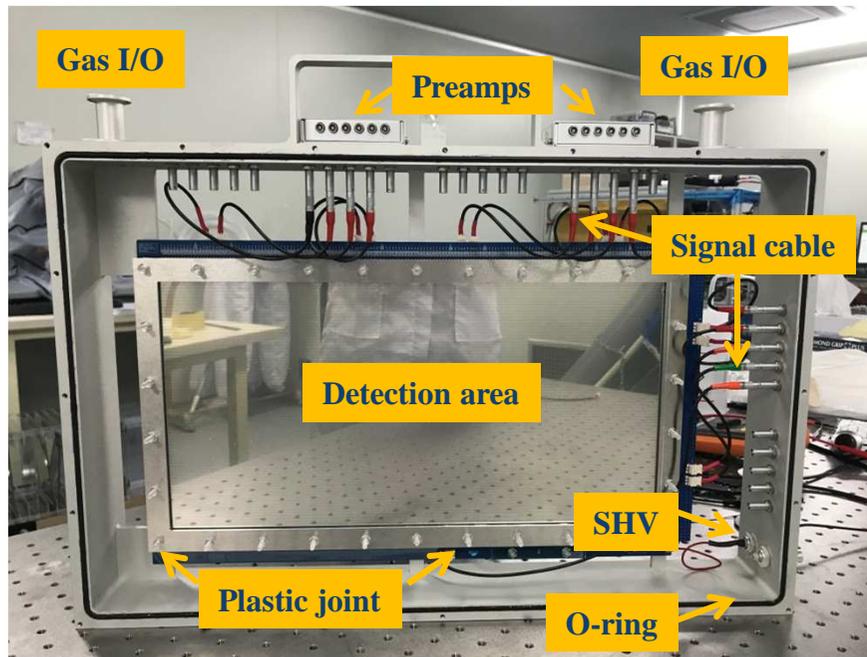
* Photolithography is a technique for complex integrated circuit, ~ few μm precision

Anode electrode

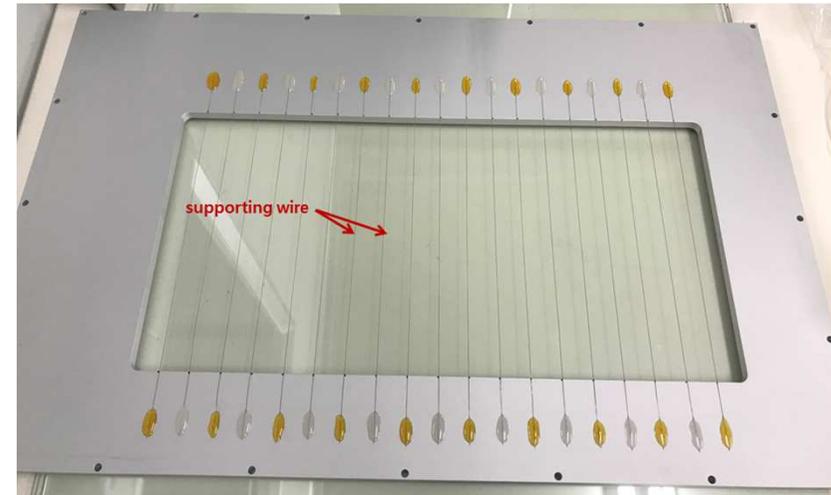


❖ Uniformity of surface is quite important to prevent discharge

Detector assembly



Inside of PPAC, 40 × 20 cm² PPAC
(covers opened)



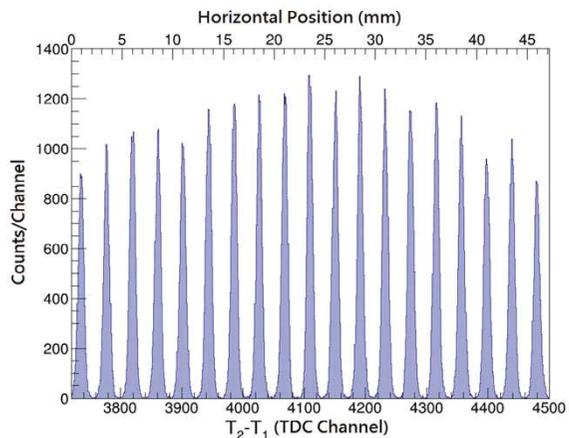
Cover with supporting wire, 40 × 20 cm² PPAC

- ❖ Single gap or double gap can be assembled
- ❖ Plastic bolts and nuts are used for electric insulation
- ❖ Housing has O-ring for gas shielding
- ❖ Peamps are put close at PPAC housing to reduce signal noise

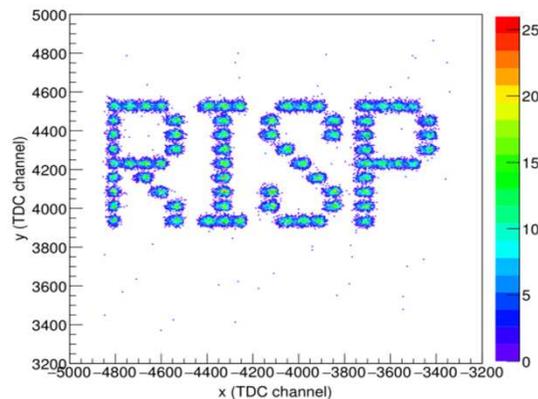
^{241}Am source test

- ❖ ^{241}Am source test of $10 \times 10 \text{ cm}^2$ PPAC
 - Position resolution $\sim 0.55 \text{ mm}$ (FWHM) (KoBRA requirement; $\sim 1.00 \text{ mm}$ (σ))
 - Time resolution $\sim 450 \text{ ps}$ (FWHM)
 - Efficiency $> 99\%$

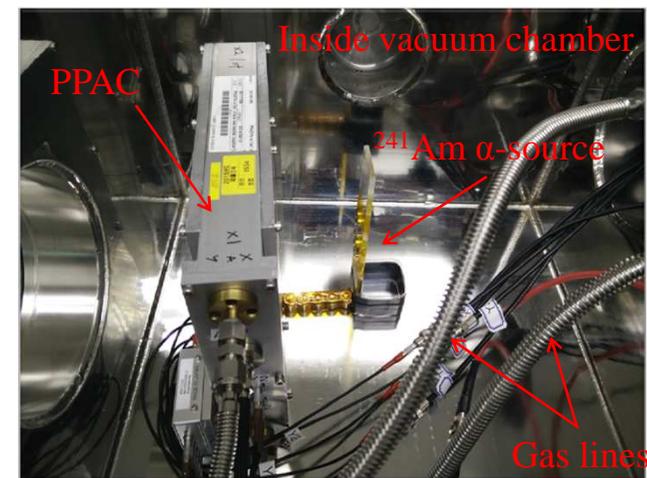
C. Akers, et al., JKPS 70 (2017) 682-696



Position structure from 2.5 mm pitch 0.5 mm width silt mask



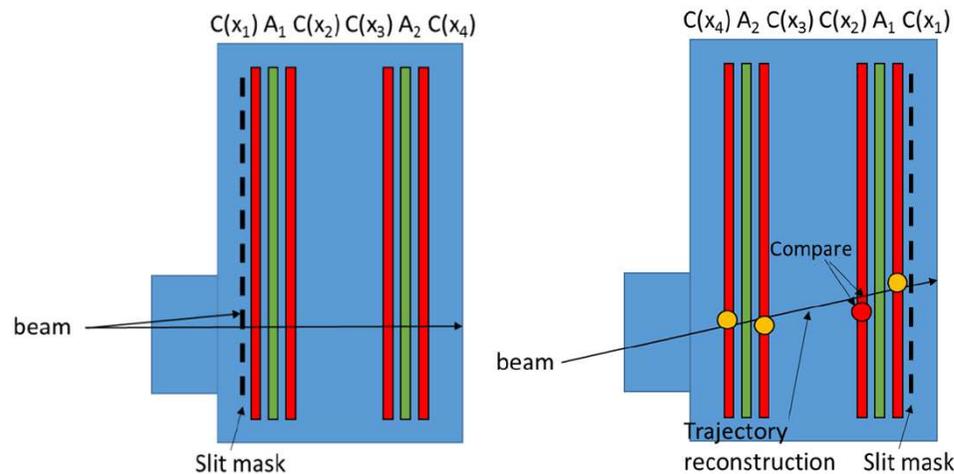
2D position spectrum displaying lettering of 'RISP'



Vacuum chamber test

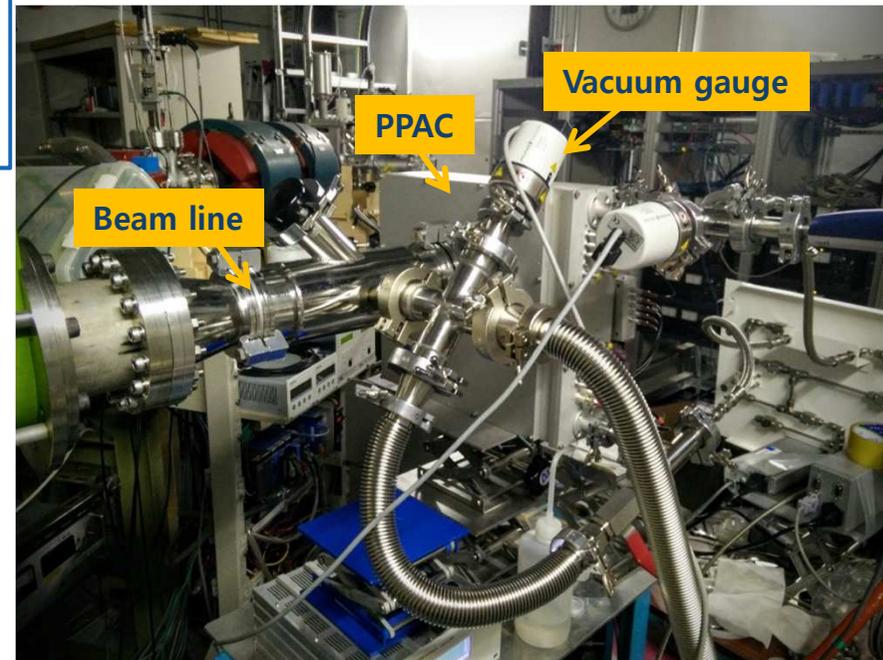
Beam test configuration

- ❖ Beam test of $20 \times 20 \text{ cm}^2$ PPAC
 - Tested at tandem accelerator of Kyushu University
 - ^{12}C and ^{16}O beam @ 3 MeV/u
 - Intensity up to 2×10^5 pps for ^{12}C and 2×10^6 pps for ^{16}O



Measurement with slit

Track fitting without slit

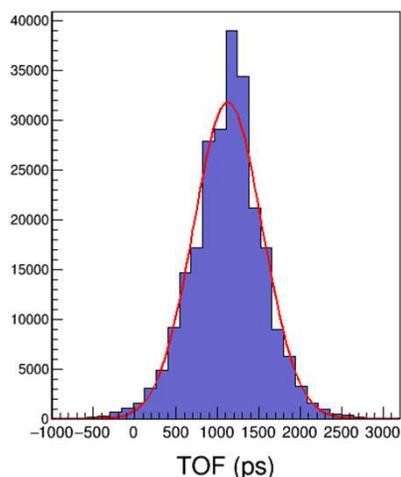
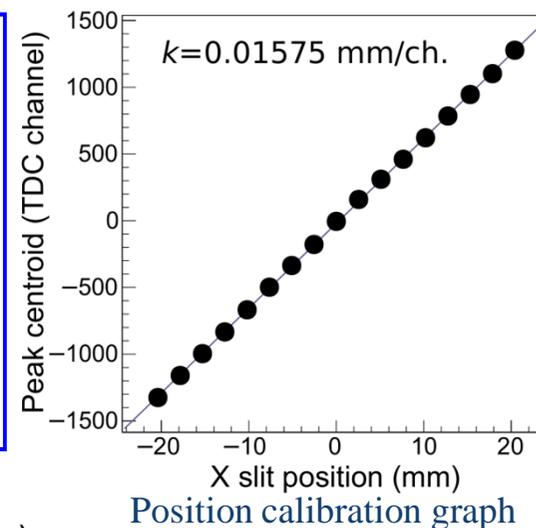


Picture of experiment configuration

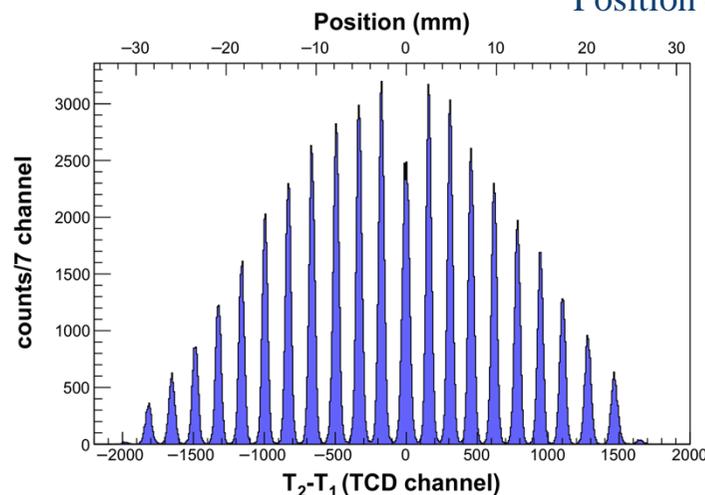
- ❖ Took two datasets from two configurations
- ❖ Double layer for track fitting & efficiency measurement

Beam test analysis

- ❖ Used slit mask to calibrate position spectrum
- ❖ Width of peaks gave position resolution
- ❖ Observed position resolution of 1 mm (FWHM) at beam intensity of 2×10^5 pps ^{12}C
- ❖ TOF between anodes gave time resolution of 700 ps (FWHM) at 2×10^6 pps ^{16}O
- ❖ Very high efficiency: 99% at 2×10^5 pps ^{12}C , 95% at 2×10^6 pps ^{16}O
- ❖ Track fitting data at 2×10^6 pps beam is being analyzed



TOF between two anodes



Position distribution with slit mask

- ❖ Large area PPAC adopted photolithographic method for cathode electrode, for the first time
- ❖ $10 \times 10 \text{ cm}^2$, $20 \times 20 \text{ cm}^2$ and $40 \times 20 \text{ cm}^2$ PPACs have been developed successfully
- ❖ Large area PPAC worked stably at high beam intensity of $2 \times 10^6 \text{ pps } ^{16}\text{O}$
- ❖ PPAC and detection system will be commissioned in ~2019
- ❖ Day-1 experiment at KoBRA facility is planned in ~2020